

Notice of References Cited	Application/Control No. 09/912,011	Applicant(s)/Patent Under Reexamination ZURASKI ET AL.	
	Examiner Latha Ravindran	Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,067,616	05-2000	Stiles et al.	712/239
	B	US-5,515,518	05-1996	Stiles et al.	712/239
	C	US-5,163,140	11-1992	Stiles et al.	711/140
	D	US-6,553,488	04-2003	Yeh et al.	712/239
	E	US-5,423,011	06-1995	Blaner et al.	712/240
	F	US-5,276,882	01-1994	Emma et al.	712/240
	G	US-6,141,748	10-2000	Tran, Thang M.	712/239
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP 798632 A2	10-1997	European Patent	YUNG, ROBERT	G06F 09/38
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Perleberg, C.H. et al., Branch Target Buffer Design and Optimization, April 1993, IEEE Transactions on Computers, volume 42, no. 4, Pages 396 - 412
	V	IBM, Partial Address Recording in Branch History Tables, IBM Technical Disclosure Bulletin, June 1, 1993, Volume 36, Issue 6B, Pages 379 - 380
	W	FOLDOC, Free Online Dictionary of Computing, http://wombat.doc.ic.ac.uk/foldoc/foldoc.cgi?query=second+level+cache , June 25, 1997
	X	FOLDOC, Free Online Dictionary of Computing, http://wombat.doc.ic.ac.uk./foldoc/foldoc/cgi?cache , June 25, 1997

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.